

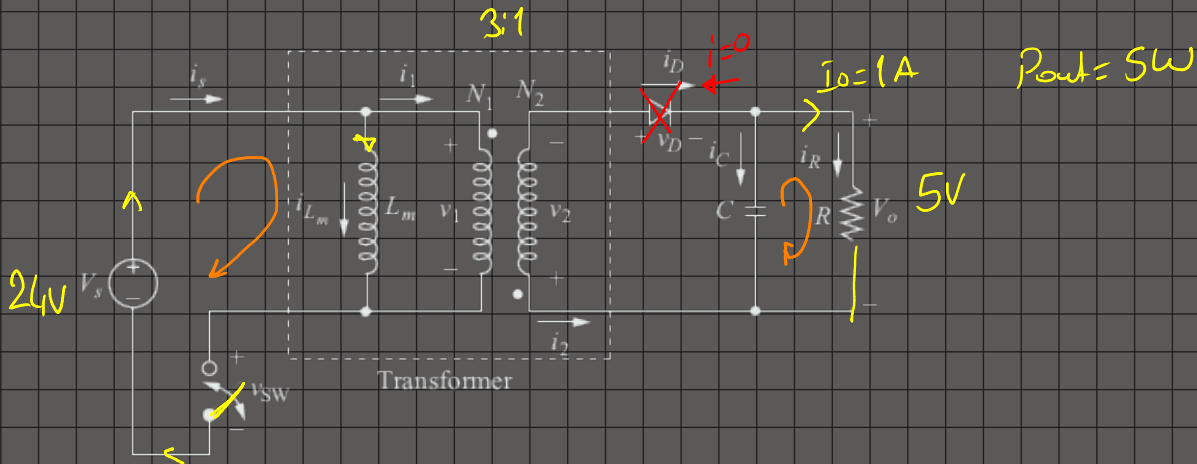
Hyd Ex. 7.1

Flyback Converter

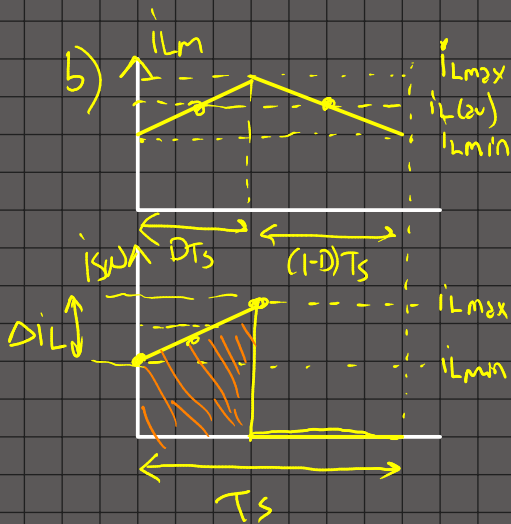
A flyback converter of Fig. 7-2 has the following circuit parameters:

- $V_s = 24 \text{ V}$
- $N_1/N_2 = 3.0$
- $L_m = 500 \text{ } \mu\text{H}$
- $R = 5 \text{ } \Omega$
- $C = 200 \text{ } \mu\text{F}$
- $f = 40 \text{ kHz}$
- $V_o = 5 \text{ V}$

Determine (a) the required duty ratio D ; (b) the average, maximum, and minimum values for the current in L_m ; and (c) the output voltage ripple. Assume that all components are ideal.



$$a) = \frac{V_o}{V_s} = \frac{D}{(1-D)} \cdot \frac{N_2}{N_1} \Rightarrow \frac{5}{24} = \frac{D}{(1-D)} \cdot \frac{1}{3} \Rightarrow D = \underline{\underline{0,385}}$$



$i_s(av) = ?$ $P_{out} = P_{in}$
 $5W = 24V \cdot i_s(av)$
 $\rightarrow i_s(av) = 5/24 \text{ A}$
 $i_s(av) = \frac{i_{Lmin} + i_{Lmax}}{2} \cdot \frac{DT_s}{T_s} = \frac{5}{24} \text{ A}$
 $i_{L(av)} = \frac{i_{Lmin} + i_{Lmax}}{2} = \frac{5}{24} \cdot \frac{1}{0,385} = \underline{\underline{540 \text{ mA}}}$

$$\Delta i_L = \frac{V_s \cdot DT_s}{L_m} = \frac{24 \cdot 0,385}{500 \mu\text{H} \cdot 40 \text{ kHz}} = \underline{\underline{460 \text{ mA}}}$$

$$L = \frac{N^2}{R}$$

$$i_{Lmin} = i_{L(av)} - \frac{\Delta i_L}{2} = 540 - \frac{460}{2} = \underline{\underline{310 \text{ mA}}} \quad i_{Lmax} = 540 + \frac{460}{2} = \underline{\underline{770 \text{ mA}}}$$

$G > 0 \Rightarrow \underline{\underline{CCM}}$

$$i_{L(\max)} \propto \hat{\phi}_{\text{core}} \propto \underline{\underline{\hat{B}_{\text{core}}}}$$

c) $\Delta V_0 = ? \Rightarrow$ Capacitor supplies the load during on period.

$$\Delta V_0 = \frac{\dot{I}_0 \cdot DT_s}{C} = \frac{1A \cdot 0,395}{200\mu 40kHz} \approx \underline{\underline{0,05V}}$$

$$\frac{\Delta V_0}{V_0} = \frac{0,05}{5} \Rightarrow \underline{\underline{1\%}} \text{ (acceptable)}$$