EE-464 STATIC POWER CONVERSION-II

Switching Power Supplies

Ozan Keysan

<u>keysan.me</u>

Office: C-113 • Tel: 210 7586

. Regulated Output Voltage

- . Regulated Output Voltage
- . Electric Isolation

- . Regulated Output Voltage
- . Electric Isolation
- . Minimum size, weight

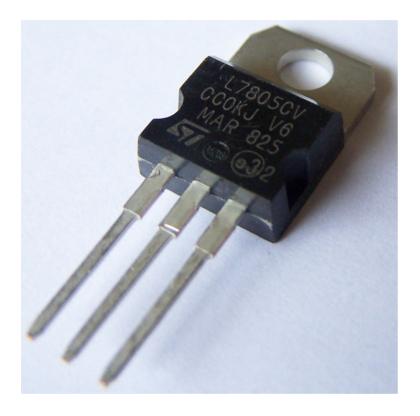


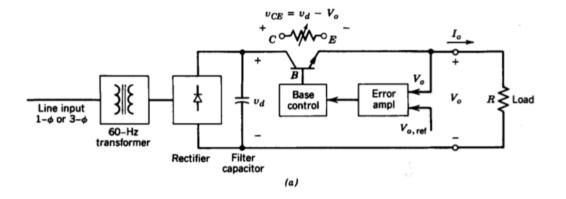
- . Regulated Output Voltage
- . Electric Isolation
- . Minimum size, weight
- . Minimum cost

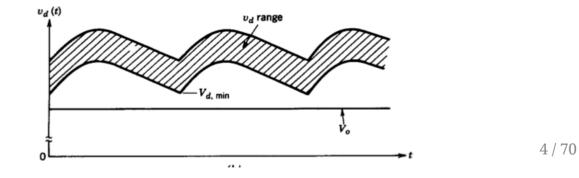
- . Regulated Output Voltage
- Electric Isolation
- . Minimum size, weight
- . Minimum cost
- . Maximum efficiency

Linear Regulators

Linear Regulators







. Low frequency transformer: large and heavy

- . Low frequency transformer: large and heavy
- . BJT operates in linear region: dissipates heat



- . Low frequency transformer: large and heavy
- . BJT operates in linear region: dissipates heat
- . Efficiency is around 30-60%

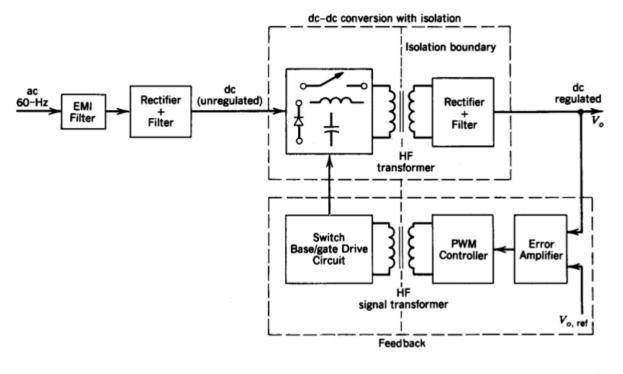


- . Low frequency transformer: large and heavy
- . BJT operates in linear region: dissipates heat
- . Efficiency is around 30-60%
- Advantage:



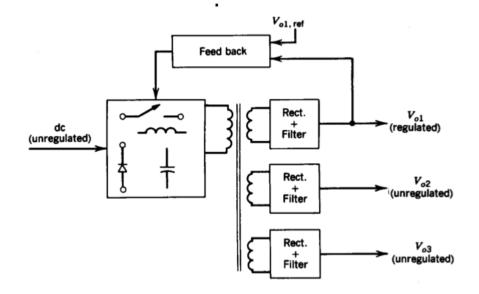
- . Low frequency transformer: large and heavy
- . BJT operates in linear region: dissipates heat
- Efficiency is around 30-60%
- . Advantage:Minimum EMI Problems





6 / 70

Multiple Output Case



7 / 70

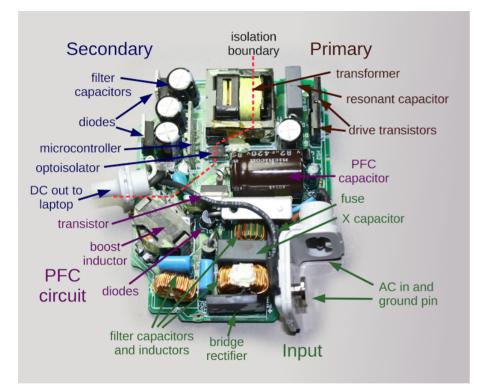
Case Study:

Case Study:

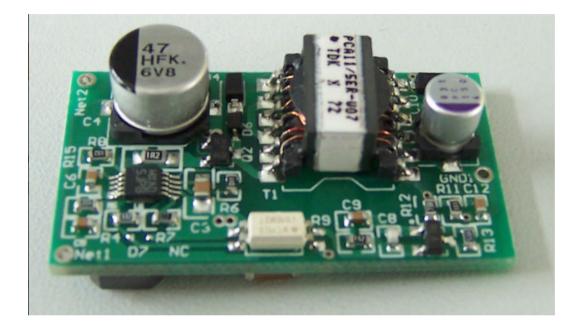
<u>Apple Charger Teardown</u>

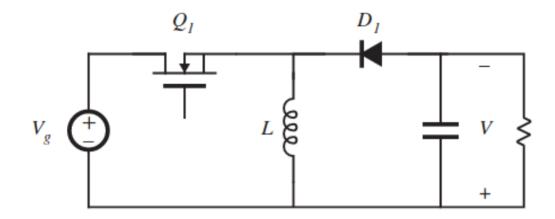


Apple Charger Teardown

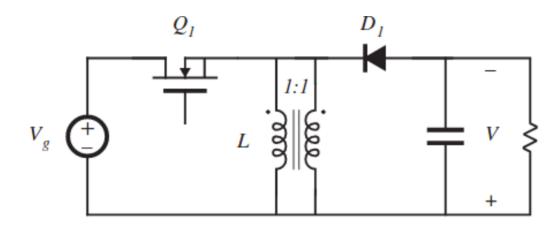


Flyback Converter



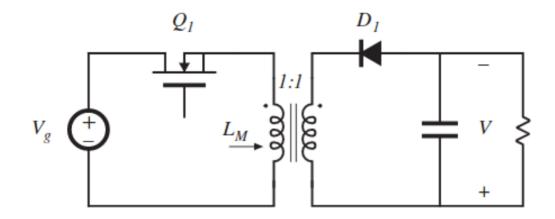


Start from the buck-boost converter

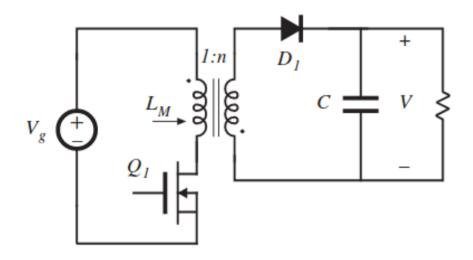


Wound the inductor with two parallel wires

Don't get confused with the dots yet!



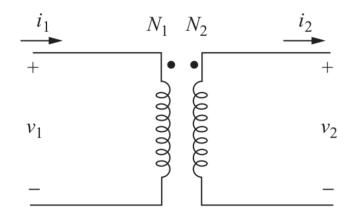
Isolate inductor wires (isolated converter)



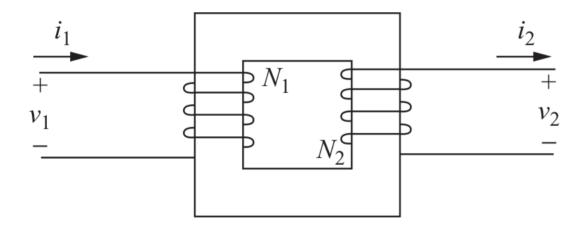
Modify turns ratio to adjust output voltage and direction to get positive output

Ideal Transformer

Ideal Transformer



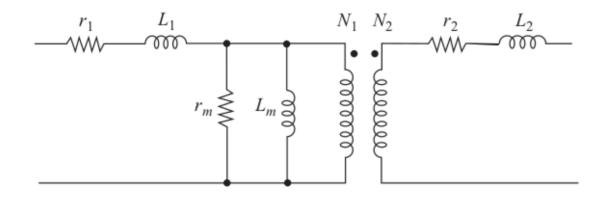
Ideal Transformer



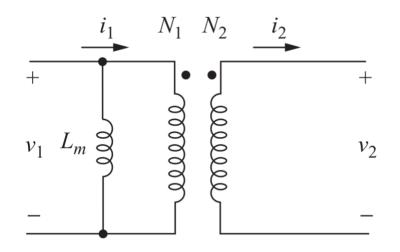
16 / 70

Realistic Transformer Equivalent Circuit

Realistic Transformer Equivalent Circuit

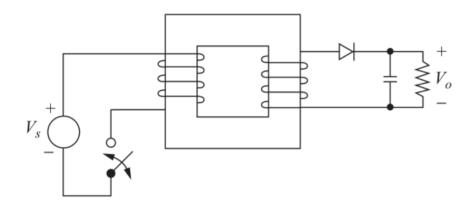


Let's ignore resistive parts and leakage flux for now

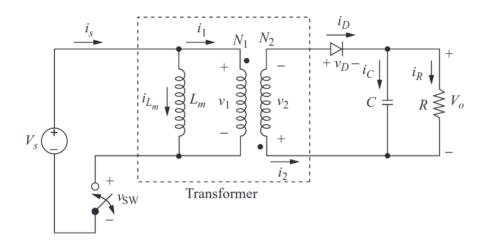


18 / 70

Flyback Converter with Transformer

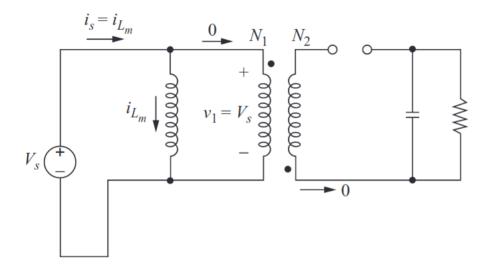


Flyback Converter with Transformer



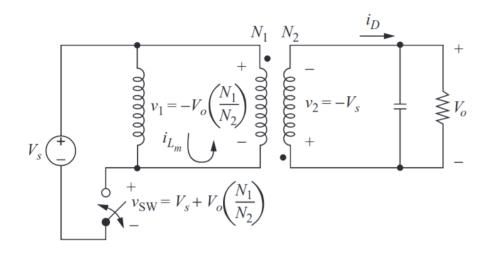
Can you plot the operating modes?

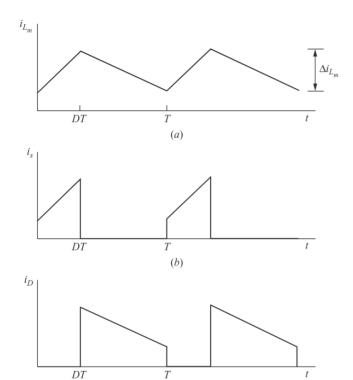
Switch ON



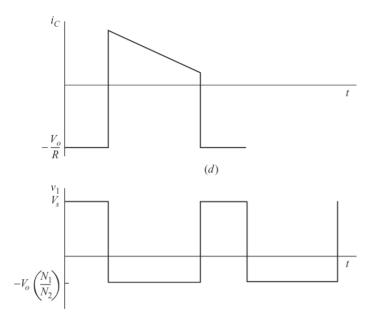
21 / 70

Switch OFF





23 / 70



24 / 70

Conversion ratio can be calculated in three different ways:

- Magnetic Circuit: Transformer Flux
- Steady state current
- Graphically: Voltage-seconds area of the inductor

Conversion ratio can be calculated in three different ways:

- Magnetic Circuit: Transformer Flux
- Steady state current
- Graphically: Voltage-seconds area of the inductor

$$rac{V_o}{V_d} = rac{D}{(1-D)} rac{N_2}{N_1}$$

Peak Switch Current

Peak Switch Current

$$\hat{I}_{sw} = rac{1}{(1-D)} rac{N_2}{N_1} I_o + rac{N_1}{N_2} rac{(1-D)T_s}{2L_m} V_o$$

Peak Switch Current

$$\hat{I}_{sw} = rac{1}{(1-D)} rac{N_2}{N_1} I_o + rac{N_1}{N_2} rac{(1-D)T_s}{2L_m} V_o$$

Peak Switch Voltage

Peak Switch Current

$${\hat I}_{sw} = rac{1}{(1-D)} rac{N_2}{N_1} I_o + rac{N_1}{N_2} rac{(1-D)T_s}{2L_m} V_o$$

Peak Switch Voltage

$${\hat V}_{sw} = V_d + {N_1 \over N_2} V_o$$

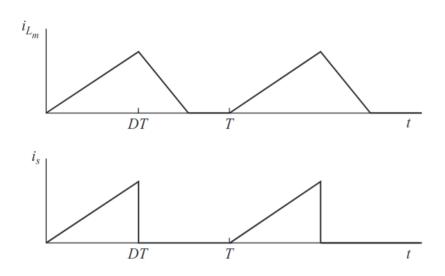
Peak Switch Current

$${\hat I}_{sw} = rac{1}{(1-D)} rac{N_2}{N_1} I_o + rac{N_1}{N_2} rac{(1-D)T_s}{2L_m} V_o$$

Peak Switch Voltage

$$\hat{V}_{sw} = V_d + rac{N_1}{N_2} V_o = rac{V_d}{(1-D)}$$

Flyback Converter: DCM



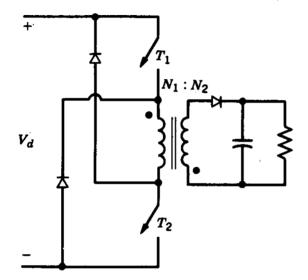
27 / 70

Reading Materials

- <u>The Flyback Converter</u>
- <u>Flyback transformer tutorial: function and design</u>
- Flyback Converter Video
- <u>Designing a DCM flyback converter</u>
- <u>Flyback DCM vs CCM</u>

Flyback Variations: Two Transistor Flyback

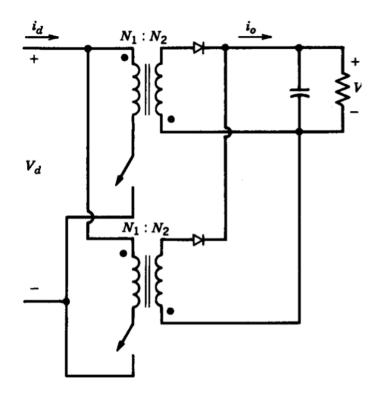
Flyback Variations: Two Transistor Flyback



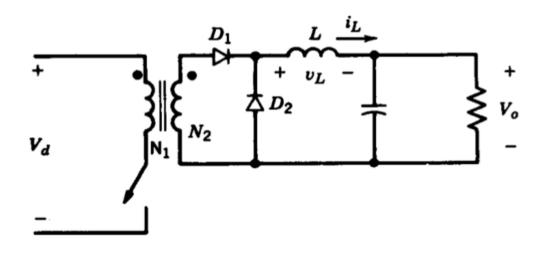
- Improving the Performance of Traditional Flyback with Two Switch Approach
- Operation & Benefits of Two-Switch Forward/Flyback Power Converter <u>Topologies</u>

Flyback Variations: Paralled Flyback

Flyback Variations: Paralled Flyback



Derived from the Buck Converter

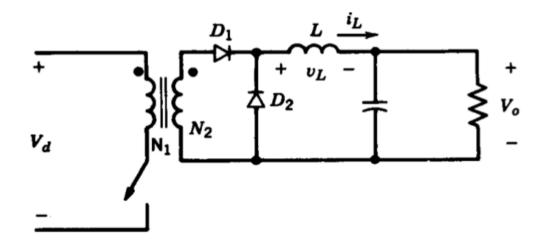


31 / 70

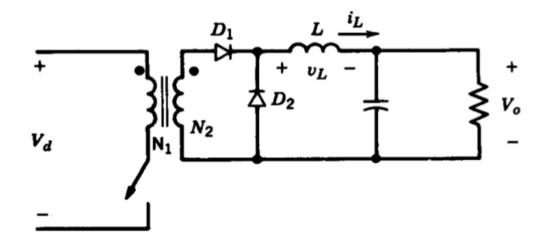
Let's obtain the output voltage characteristics

A buck converter with added turns ratio

$$rac{V_o}{V_d} = rac{N_2}{N_1} D$$

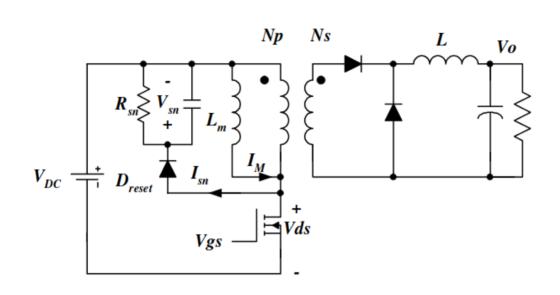


What happens at the instant when the switch is turned-off, if the transformer is not ideal?



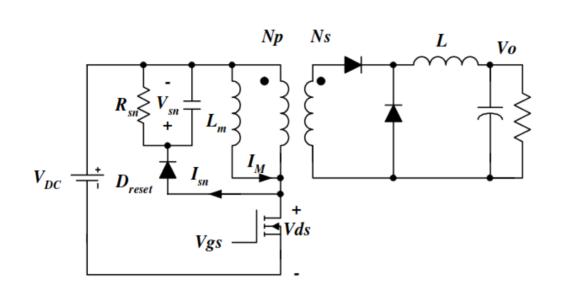
A discharging path for Lm should be added.

Simple Solution: RCD Reset Circuit



Magnetizing current dissipates through RCD circuit

Simple Solution: RCD Reset Circuit



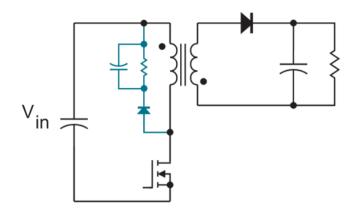
Cheap but inefficient

RCD Snubber

Note a similar circuit can be used for the Flyback converter (to reduce inductance leakage ringing)

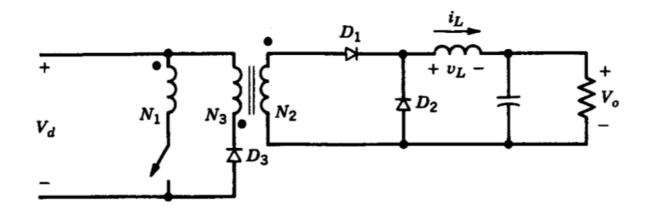
RCD Snubber

Note a similar circuit can be used for the Flyback converter (to reduce inductance leakage ringing)



Suggested Reading: <u>Flyback Converter Snubber Design</u>

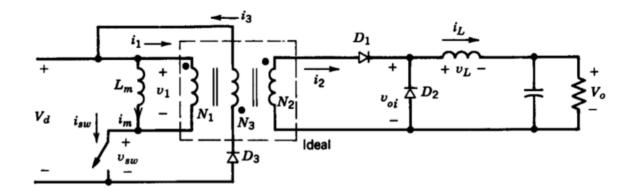
Practical Forward Converter



A transformer with two-primary windings

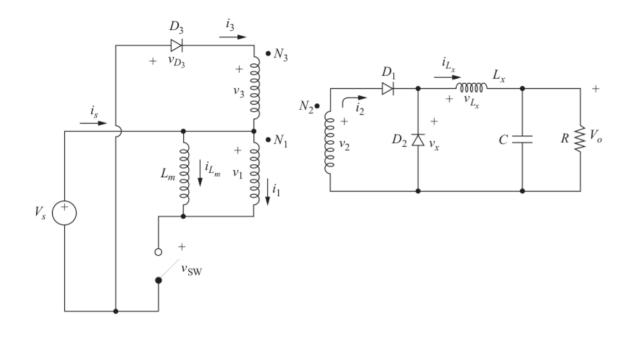
Third winding is added to discharge the energy stored in Lm

Practical Forward Converter



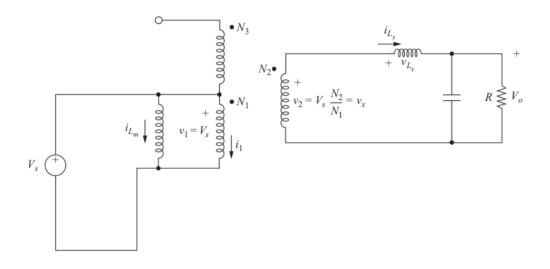
A transformer with two-primary windings

Third winding is added to discharge the energy store



41 / 70

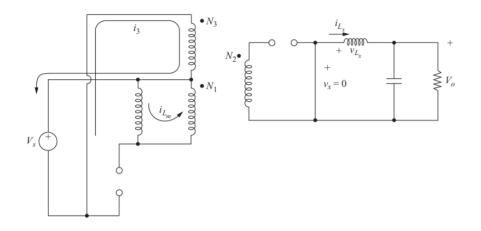
Forward Converter: Switch is ON



Lm is charged by input voltage, Lx is also charging

D1 On, D2 Off, D3 Off

Forward Converter: Switch is OFF



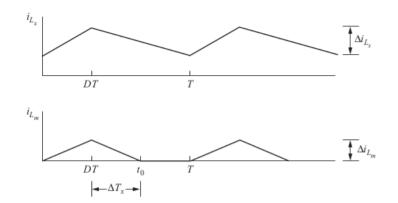
Lx feeds the load, Lm is discharged to the source: $i_1=-i_{Lm}$

KCL:
$$N_1 i_1 = N_2 i_2 - N_3 i_3$$

For proper operation the transformer should be "reset" before next 0N period $^{\rm 43\,/\,70}$

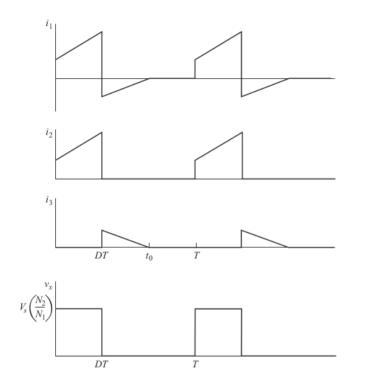
Forward Converter: Switch is OFF

Forward Converter



45 / 70

Forward Converter



46 / 70

Practical Forward Converter

For proper operation the transformer should be "reset" before next ON period

$$t_m < (1-D)T_s$$

47 / 70

Practical Forward Converter

For proper operation the transformer should be "reset" before next ON period

$$t_m < (1-D)T_s$$

 D_{max}

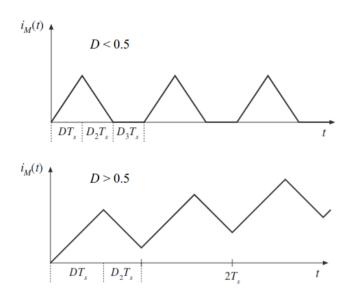
Practical Forward Converter

For proper operation the transformer should be "reset" before next ON period

$$t_m < (1-D)T_s$$
 $D_{max} = rac{1}{1+(N_3/N_1)}$

What happens if D is large, and transformer does not reset completely?

What happens if D is large, and transformer does not reset completely?



In the figure Dmax=0.5

Saturation, increased core losses, reduced Lm, problem in power transfer

Advantages over Flyback

- Better utilization of transformer (direct power transfer, higher)
- A gapless core can be used (higher Lm, less ripple)
- Output inductor and diode ensures continuous output current

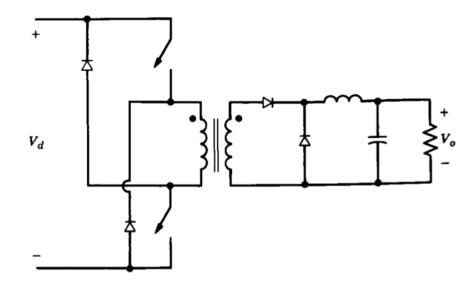
Drawbacks compared to Flyback

- . Increased cost (extra diode and inductor)
- . Gain changes a lot in DCM
- . Higher voltage requirement for MOSFET

Forward Converter Alternatives

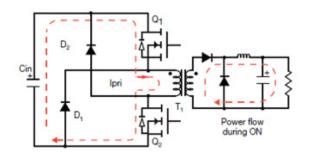
Forward Converter Alternatives

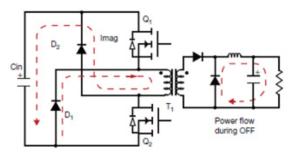
Two-switch forward converter



Forward Converter Alternatives

Two-switch forward converter





52 / 70

Two-switch forward converter

Advantages:

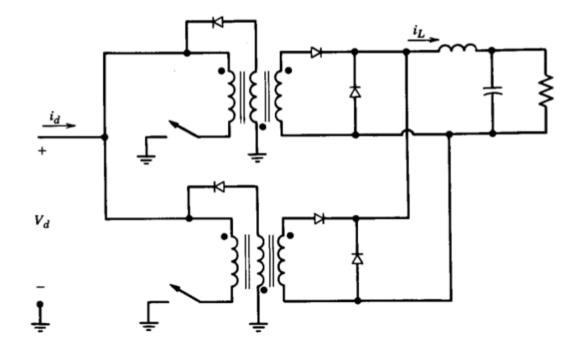
- Does not require a snubber circuit
- Less voltage stress on MOSFETs
- Can supply multiple isolated outputs
- Low power losses and noise

Two-switch forward converter

Disadvantages:

- Slightly more expensive
- Larger component count

Interleaved forward converter



55 / 70

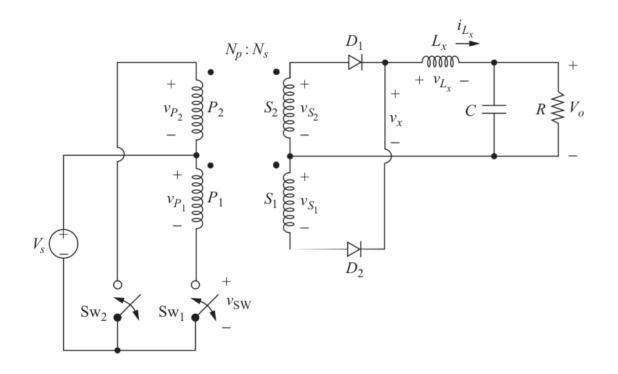
Forward Converter Reading Materials

- Infineon, Forward Converter Design Note
- Fairchild Forward Converter Application Note

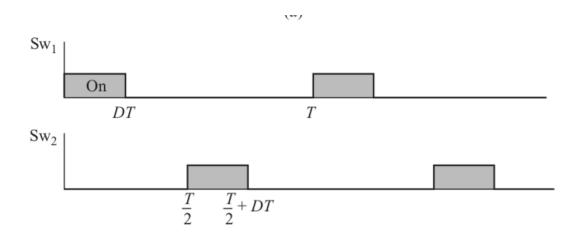
Example:

Hart - Power Electronics

Ex. 7-4



Uses a center-tapped transformer



Three operating sections

Switch(T1) ON, Switch(T2) OFF

D1 conducts, D2 reverse-biased

Switch(T1) ON, Switch(T2) OFF

D1 conducts, D2 reverse-biased

$$v_x = rac{N_2}{N_1} V_s$$

Switch(T1) ON, Switch(T2) OFF

D1 conducts, D2 reverse-biased

$$v_x = rac{N_2}{N_1} V_s$$

$$v_L=v_x-V_o=rac{N_2}{N_1}V_s-V_o$$

 i_L increases linearly

Switch(T2) ON, Switch(T1) OFF

Symmetrical operation with the previous

$$v_x = rac{N_2}{N_1} V_s$$

Switch(T2) ON, Switch(T1) OFF

Symmetrical operation with the previous

$$v_x = rac{N_2}{N_1} V_s$$

$$v_L=v_x-V_o=rac{N_2}{N_1}V_s-V_o$$

 i_L increases linearly

Both Switches are OFF

for a period of Δ

Both Switches are OFF

for a period of Δ

Both D1 and D2 conducts

 $I_{D1} = I_{D2} = 0.5 I_L$

 $v_x = 0$

Both Switches are OFF

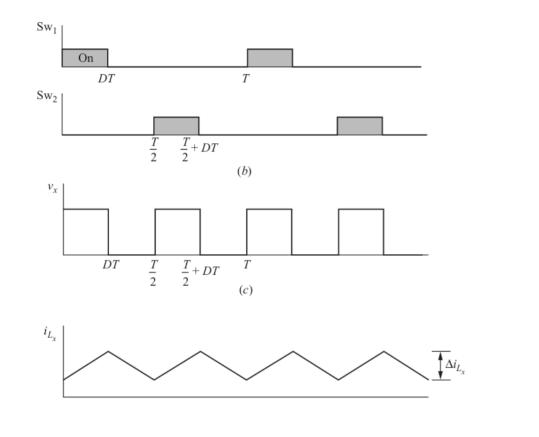
for a period of Δ

Both D1 and D2 conducts

 $egin{aligned} I_{D1} &= I_{D2} = 0.5 I_L \ v_x &= 0 \end{aligned}$ Therefore $v_L = -V_o$

Inductor discharges and feeds the load

Repeating waveforms for every Ts/2



Repeating waveforms for every Ts/2

$$DT_s+\Delta=rac{T_s}{2}$$
 $\Delta=rac{(1-2D)}{2}T_s$

64/70

Output voltage characteristics?

Output voltage characteristics?

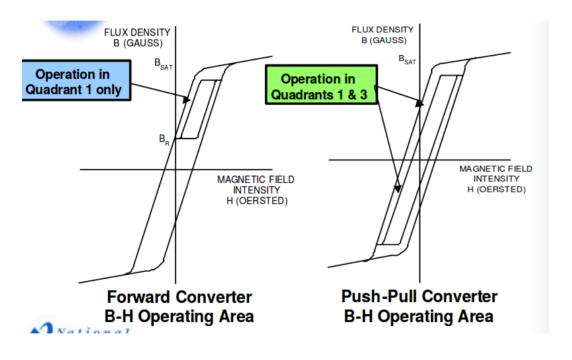
Use the inductor voltage

Output voltage characteristics?

Use the inductor voltage

$$rac{V_o}{V_d} = rac{2N_2}{N_1}D$$

Twice of the forward converter



Comparison of Magnetic Flux in the Core

66 / 70

Extra Materials

Flyback Converter

ECEN4517 Lecture Notes

Flyback Transformer Tutorial

Optimised Flyback Design

Switch Mode Power Supply (SMPS) Topologies

ECE5797 SMPSs

Flyback Converter, Transformer Design

Design Guide Flyback Converter

Design Guidelines for Flyback Converter

Transformer Design Cookbook

Extra Materials

Forward Converter

Forward Converter, Transformer, Inductor Design

Forward Converter Design

Forward Converter Tutorial Video

Design Exercise

Design Exercise

Forward Converter Design

ETD 34/17/11

Design Exercise

Forward Converter Design

ETD 34/17/11

Skin Effect Calculator

AWG Conductors

You can download this presentation from: <u>keysan.me/ee464</u>